

A New Technique for Elimination of Harmonics Using Three Phase Shunt Active Filter

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Abstract: This paper describes the current-detection algorithm based on the time-domain approach for three-phase shunt active power filters (APFs). It aims at eliminating harmonics and/or correct power factor and/or balance asymmetrical loads are analyzed. An overview and assessment of the performance of existing current-detection algorithms for active power filters are presented. According to different complicated power quality issues and various compensation purposes, a new current-detection algorithm is recommended. Different compensating current references can be accurately obtained by adopting the proposed algorithm. It make sure that the shunt APF can very well achieve different compensation purposes. Simulation results obtained with MATLAB and testing results on an experimental shunt APF are presented to validate the proposed algorithm.

Key words: Active Power Filter (APF) • Current-detection algorithm • Harmonic compensation • Reactive power compensation • Time domain

INTRODUCTION

With the proliferation of nonlinear loads such as diode / thyristor rectifiers, non-sinusoidal currents degrade power quality in power transmission/distribution systems. Traditionally, passive filters have been used to attenuate the harmonic distortion and compensate the active power, but passive filters are bulky, detune with age and can resonate with the supply Impedance. The active power filters are great tools for the compensation not only of current harmonics produced by distorting loads but also of reactive power and unbalance of nonlinear and fluctuating loads [1]. The shunt APFs (SAPFs) are used most widely. The performance of SAPF strictly depends on the features of the current-detection algorithms and controllers. A novel current-detection algorithm of SAPF for harmonic elimination, power factor correction and balancing of nonlinear loads is proposed [2-4].

Traditional and Proposed Current Detection Algorithms: The existing methods of current-detection algorithm are, Analogue methods, in which recision is not satisfactory (i.e.) the derived component has magnitude and phase

errors, Instantaneous Reactive Power Theory-Based method requires co-ordinate transformations between the a,b,c and p,q coordinates which increases the complexity of designing the APF controller, Fourier Transform Based Methods, which has good precision but one complete main cycle delay is unavoidable for the algorithm, Modified Fourier transform-based methods can improve dynamic response, but one main cycle is required to track the load change completely, Other methods such as fuzzy control, adaptive and the wavelet and neural network algorithms have better dynamic response than FFT, but they require a large amount of calculation [5]. The Proposed algorithm based on time domain for determining the APF reference compensating currents has response time delay less than one main cycle which is high in DFT and in IRPT. This algorithm does not need the coordinate transformation as in IRPT. Comparing with existing algorithms, this algorithm has shorter response time delay.

Shunt Active Power Filter Control System: The derived mathematical equations can be implemented using MATLAB/Simulink. The control system of the proposed current detection algorithm is shown in Fig. 1.

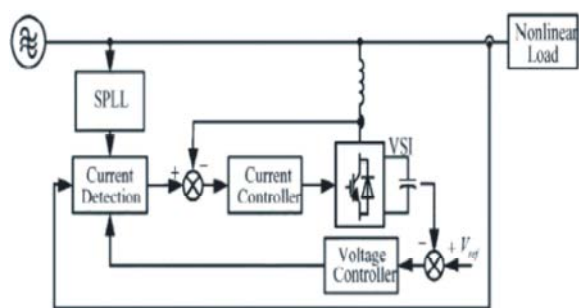


Fig. 2.1: SAPF Control System

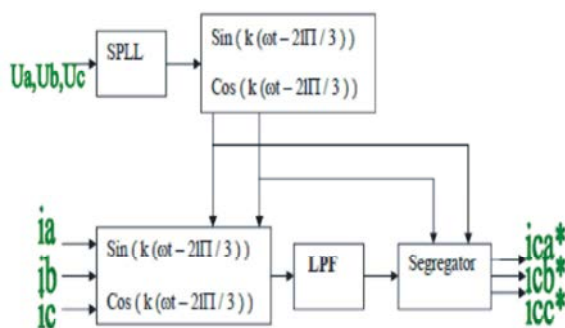


Fig. 2: Block diagram of the proposed current detection algorithm

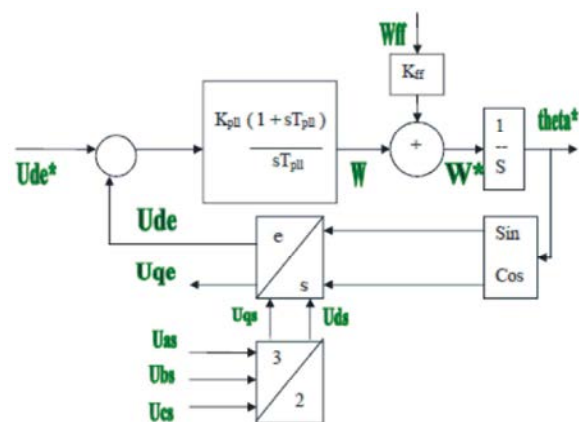


Fig. 3: Control diagram of the phase locked loop

Operation Principle of the Proposed Algorithm: In the three-phase three-wire system, the instantaneous load currents of phase “a,” “b,” “c” (i_a, i_b and i_c) can be disassembled into positive-sequence and negative-sequence components according to the symmetrical weigh law, which was proposed by Fortescue separately.

$$i_x(n) = \sum_{l=1}^x \left[I_{lk} \sin \left(\frac{2\pi nk}{N} + \varphi_{lk} - \frac{2l\pi}{3} \right) + I_{2k} \sin \left(\frac{2\pi nk}{N} + \varphi_{2k} + \frac{2l\pi}{3} \right) \right]$$

where

$$I = \begin{cases} 0 & x = a \\ 1 & x = b \\ 2 & x = c \end{cases}$$

The subscripts 1 and 2 represent the positive-sequence and negative sequence, respectively. ' K ' represents harmonic order. ' T ' Represents the peak value of the current and ' ϑ ' is the initial phase (' ϑ ' is determined as the initial phase between the positive sequence fundamental voltage of phase "a" in order to explain the physical meaning). ' N ' is the number of sampling point in one fundamental cycle and ' n ' is the count value of sampling ($n=0, 1, \dots, N-1$). Commonly, only the positive-sequence, negative-sequence, active power and reactive power of the fundamental current are cared and it is not necessary to decompose the harmonic. Then, the fundamental current component is expressed as follows

$$\begin{aligned} i_{x1}(n) &= I_{11} \sin\left(\frac{2\pi}{N}n + \varphi_{11} - \frac{2l\pi}{3}\right) + I_{21} \sin\left(\frac{2\pi}{N}n + \varphi_{21} + \frac{2l\pi}{3}\right) \\ &= I_{11} \sin\left(\frac{2\pi}{N}n - \frac{2l\pi}{3}\right) \cos\varphi_{11} \\ &\quad + I_{11} \cos\left(\frac{2\pi}{N}n - \frac{2l\pi}{3}\right) \sin\varphi_{11} \\ &\quad + I_{21} \sin\left(\frac{2\pi}{N}n - \frac{2l\pi}{3}\right) \cos\left(\varphi_{21} - \frac{2l\pi}{3}\right) \\ &\quad + I_{21} \cos\left(\frac{2\pi}{N}n - \frac{2l\pi}{3}\right) \sin\left(\varphi_{21} - \frac{2l\pi}{3}\right) \end{aligned}$$

Fig. 2 shows the block diagram of the proposed current-detection algorithm, where ' $\sin \frac{2\pi}{3}n$ ' is synchronous with the positive-sequence fundamental voltage of phase "a," which determines the calculation precision of active and reactive power components. The low-pass filter used determines the performance of the system. According to different compensation purposes, the segregator will obtain different components expediently, which is superior to the algorithm based on the instantaneous reactive power theory. The synchronous signals can be obtained by soft phase loop lock (SPLL).

Principle of Operation of Spill: reference frame voltages are then transformed to voltages U_{de} , U_{qe} using the $3/2$ and e / s transformations. The angle θ used in these transformations is obtained by integrating a frequency

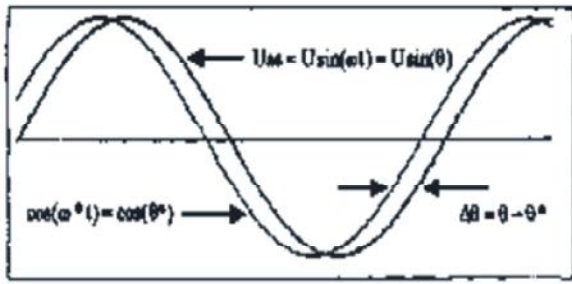


Fig. 4: Input phase voltage and PLL output

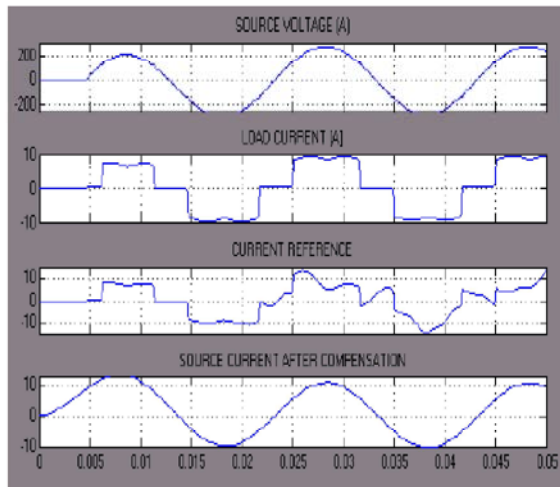


Fig. 5: Source voltage, load current, Current reference and Source current after compensation for phase A

command ω . If the frequency command ω is identical to the utility frequency, the voltages U_{de} and U_{qe} , appear as dc values depending on the angle θ . In this method, a PI regulator can be obtained that value of θ (or ω) which drives the feedback voltage U_{de} to a commanded value U_{de}^* . The Magnitude of the controlled quantity U_{de} determines the phase difference between the utility voltages and $\sin(\theta)$ or $\cos(\theta)$. The method results not only in the utility frequency ω but also allows one to lock at an arbitrary phase angle θ^* with respect to the utility angle θ . The angle $\Delta\theta$ (Fig. 4.) is controlled by the commanded values U_{de}^*

The basic configuration of the PLL system is shown in Fig. 3. The phase voltages U_{as} , U_{bs} , U_{cs} are obtained from sampled line-to-line voltages [6]. These stationary

Simulation Results

Simulation Conditions: The purpose of the simulation is to show the usefulness of the proposed SAPF control strategy. Two test cases are taken into consideration with different source voltages and load

conditions [7]. The source voltages are sinusoidal and balanced with a magnitude of 230 V and a frequency of nonlinear load.

Simulation Results for Sinusoidal, Balanced Source Voltages: The balanced and sinusoidal three phase voltages considered are,

$$V_a = 230 \sin(\omega t)$$

$$V_b = 230 \sin(\omega t - 120^\circ)$$

$$V_c = 230 \sin(\omega t + 120^\circ)$$

The load used is a bridge rectifier which acts as a nonlinear imbalanced load. The simulation results have been plotted separately for a clear study [8]. Fig.6 exhibit the source voltage, line current, reference compensation current and source current after compensation for phase A. Similarly the same shape of waveforms are obtained for remaining phases.

CONCLUSION

The thesis has outlined the mathematical modeling and design of the reference compensation current controllers for shunt active power filters based on time domain approach in detail. The simulation results of the proposed method are compared with that of the available results of Generalized Instantaneous Reactive Power Theory based method, Synchronous Reference Frame method and the Synchronous Current Detection methods [9-13]. From the results it can be concluded that the delay resulting from the proposed algorithm is less than half of the main cycle, which is half of that of DFT and the same as that of the algorithm based on IRPT. From the analysis and simulation it is found that the algorithm presented in this thesis has the advantages of flexibility, accuracy and easy implementation. Since the reference compensation currents are determined in the 'a-b-c' reference frame, there is no reference frame transformation is required. Therefore, it results in less complexity in realizing the control circuit of SAPF and still maintains good filter performance.

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